

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Shunpei Yamazaki et al. Art Unit : 2629  
Serial No. : 09/504,235 Examiner : David L. Lewis  
Filed : February 15, 2000 Confirmation No.: 7610  
Notice of Allowance Date: February 22, 2007  
Title : METHOD OF MANUFACTURING A SEMICONDUCTOR DEVICE

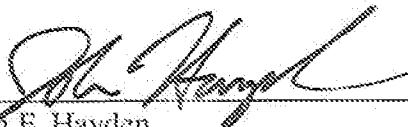
**MAIL STOP ISSUE FEE**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**RESPONSE TO NOTICE OF ALLOWANCE**

In response to the Notice of Allowance mailed February 22, 2007, enclosed is a completed issue fee transmittal form PTOL 85b. A fee in the amount of \$1430 was paid on February 10, 2005 for the issue fee and patent copies. The Director of the U.S. Patent & Trademark Office is requested to apply the previously paid fee to the application identified above.

Please apply any additional charges or credits to our Deposit Account No. 06-1050.

Respectfully submitted,

  
John E. Hayden  
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Date: May 21, 2007

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